## **EAST Search History**

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	2	"5191300".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2007/10/28 21:58
S2	107	375/345.ccls. and ((amplifier with (gain)) same ((select\$4 or choos\$4) near4 gain))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/10/28 22:03
<b>S</b> 3	171335	S2 and "MOS" or "CMOS"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/10/28 22:00
S4	12	S2 and ("MOS" or "CMOS")	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/10/28 22:03
S5	7598	((amplifier with (gain)) same ((select\$4 or choos\$4) near4 gain))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/10/28 22:03
S6	1178	S5 and ("MOS" or "CMOS")	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/10/28 22:05
<b>S7</b>	350	S5 and (gain with equaliz\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/10/28 22:47
S8	57	S5 and (gain same ("RC" adj1 filter))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/10/28 22:05
S9	24	S8 and ("MOS" or "CMOS")	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/10/28 22:19
S10	31	S7 and ("MOS" or "CMOS")	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/10/28 22:19
S11	16	((equaliz\$4) with (printed adj2 circuit adj1 board)) and (integrated near3 circuit) and (transmitter and receiver)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/10/28 22:54

## **EAST Search History**

S12	. 2	((equaliz\$4 with (interference or noise)) same (printed adj2 circuit adj1 board)) and ((integrated near3 circuit) same (transmitter and receiver))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/10/28 22:56
S13	12	(equaliz\$4 same (printed adj2 circuit adj1 board)) and ((integrated near3 circuit) same (transmitter and receiver))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/10/28 22:56